

# Dr M.Madhusudhan Reddy

✉: mmsr244@gmail.com

☎: +919949213068



## Objective:

Seeking for a challenging position in a progressive organization, this gives me scope to apply my skills and knowledge and works towards the growth of organization.

## Personal Skills:

Good at Analyzing, Understanding and Explaining.

Ability to teach any subject related to Technical Engineering.

## Professional Summary:

- Having **16 years 6 months** of academic experience.
- Working as **Associate Professor and HOD** in Dr. K V S R I T in Dept of EEE.
- Worked as **Exam section In-charge** for 2 Academic Years (1<sup>st</sup> DEC 2012 – 30<sup>th</sup> DEC 2014).
- Worked as **Principal** for Dr. K V S R I T- II shift Polytechnic College for 1.5 Years (1<sup>st</sup> JAN 2019 – 30<sup>th</sup> JUNE 2020).
- Ratified as assistant professor in Dr. K V S R I T in Dept of EEE in the A.Y of 2014-15 (LT. No. DAAO /D4/ Faculty Ratification of Affiliated Colleges/2014-15) on Date:17-04-2015
- In-charge for **Criteria-V in NAAC** in college level.
- **NBA Coordinator & In-Charge for Criteria-IV (NBA)** in the Department.
- In-Charge for all **ANNEXURES** in Autonomous work.
- Worked as Project Coordinator.
- Conducted workshops and Symposiums at national level.
- Included in planning and development of e-learning material.
- Developed Laboratory manuals for Electrical Machines, Electrical Measurements & Electrical Circuits Control Systems Labs.
- Guided several B. Tech and M. Tech Projects.
- Worked in Installation of Laboratories
- Quick-grasping and Self-learning professional with value-added attitudes
- Flexible, Diplomatic and able to co-ordinate to achieve common goals and business objectives
- Self Motivated and go-getter and ability to work in a team or as an individual.
- Conducted Several Personality Development Programs in the college.

## Education Qualifications:

**Doctor of Philosophy :**

2024

Power Quality (EEE)

K L Deemed to be University

Green Fields, Vaddeswaram, Guntur, Andhra Pradesh 522502

**Post Graduation:** 2012 82.2%  
M.Tech.E.E.E (EPS)  
St.Marry's college of Engineering and Technology.  
Deshmukhee, PochamPalli (vil), Nalgonda (Dist), Hyd.  
Jawaharlal Nehru Technology University (JNTU), Hyderabad, AP

**Graduation:** 2008 70.1%  
B.Tech, E.E.E  
Sri KottamTulasi Reddy Memorial College of Engineering and Technology.  
Kondair, Andhra Pradesh.  
Jawaharlal Nehru Technology University (JNTU), Hyderabad, AP.

**Intermediate:** 2004 80%  
Vavilala Junior College.  
Kurnool, Andhra Pradesh.  
Board of Intermediate Education, AP.

**Secondary School Certificate (SSC):** 2002 82%  
Sri Sai Siddhartha High School.  
Kurnool, Andhra Pradesh.  
Board of Secondary Education, AP.

**Overall Teaching Experience:**

#1: Worked as an Assistant Professor in the Dept. of EEE and Academic Time-Tables In-Charge in KKIT, Kurnool for the academic year, 2008 (1<sup>st</sup> JUNE 2008 – 26<sup>th</sup> JUNE 2009).

#2: Worked as an Assistant Professor in the Dept. of E.E.E in Dr.KVSRIT, Kurnool for the academic year (27<sup>th</sup> JUNE 2009 – 30<sup>th</sup> NOV 2010).

#3: Worked as an Assistant Professor & HOD in the Dept. of E.E.E in Dr.KVSRIT, Kurnool for the academic year (1<sup>st</sup> NOV 2012 – 8<sup>th</sup> AUG 2016).

#4: Worked as an Assistant Professor in the Dept. of E.E.E in GNITC-Autonomous, Hyderabad for the academic year (9<sup>th</sup> AUG 2016 – 31<sup>st</sup> OCT 2017).

#5: Presently working as Associate Professor & HOD in the Dept. of E.E.E in Dr.KVSRIT, Kurnool for the academic year (1<sup>st</sup> NOV 2017 – Till date).

**Workshops Conducted/Attended:**

#1: Coordinator for Project Expo **TECHNOVATION-2K22**. 6<sup>th</sup> & 7<sup>th</sup> Dec-2022 at Dr KVSRIT.

#2: Conducted "A Two Day National Level Workshop On Electrical Systems in Construction Industry" on 28<sup>th</sup> & 29<sup>th</sup> OCT-2022 at Dr KVSRIT

#3: Coordinator for national level conference NCIRET 2015. at Dr KVSRIT.

#4. Attended National Programme on Technology Enhanced Learning (IIT Madras) on 19/09/2015 in GPREC- Kurnool.

#5. Participated in FDP on Smart Grid Technology and Implementation of Artificial Neural Networks, in GNIT-Hyd

#6: Coordinator for A Two Day National Level Workshop on

**”Humanoid Design and Control” on 23<sup>rd</sup> and 24<sup>th</sup> Feb-2015 in KVSRRIT**

#7: Co-Convener for One Day National Level Workshop on ETAP, 2014

#8: In-Charge for Internal PPT (SPARKS, 2014).

#9: Convener for One Day National Level Symposium KITES-2013

#10: I was the Coordinator for **ETHICAL HACKING** workshop conducted by

**KRIYON DIGITAL SECURITIES (IIT, Kharagpur) at Dr.KVSRRIT in 2012**

#11: Participated in Short-term course on RECENT TRENDS IN POWER SYSTEMS (RTPS)

#12: Participated in Short-term course on Power System Reliability & FACTS

#13: Participated in National Seminar on Nuclear Technology in the development of Nation

on 21<sup>st</sup> Feb-2009

#### **International Conferences:**

#1: “Detection and Classification of Voltage Sag Using Adaptive Decomposition and Wavelet Transformation. In Electrical Engineering and Computer Science (ICEECS) held at Goa on 16th June, 2012,

# International Conference on Smart Grids, Structures and Materials (ICSGSM-2021 International Conference). Organised by Dept. of EEE, KL Deemed to be University on 19<sup>th</sup> & 20<sup>th</sup> April -2021.

#### **National conferences:**

#1. Enhanced Phase Locked loop Based Control Technique for Interline Unified Power Quality Conditioner. In NCIRET -2015, April 25<sup>th</sup> at Dr KVSRRIT.

#### **International Journals:**

# Ant Colony Optimization Technique Based Harmonic Suppression in Active Power Filters. International Journal of Integrated Engineering. ISSN: 2229-838X e-ISSN: 2600-7916. 2024.

#. Improving gain of real time PI controller using particle swarm optimization in active power filter. Microprocessors and Microsystems. Received 30 November 2021; Received in revised form 7 November 2022; Accepted 2 January 2023, Available online 4 January 2023.

# Central Force Optimization Technique based Harmonic Mitigation in Shunt Active Power Filters. International Journal of Intelligent Systems And Applications In Engineering. ISSN:2147-6799214.

# A Patent on Harmonic Suppression in Shunt Active Power Filter Using PQ And SRF Algorithms With Balanced Source Voltage. Application number: 202341063727 . Date: 29/09/2023

# A Patent on Harmonic Suppression in Shunt Active Power Filter Using PQ And SRF Algorithms With Unbalanced Source Voltage.

# Power Quality Improvement Using PQ and SRF Algorithms In Shunt Active Power Filter (SAPF) with Unbalanced Source Voltage. *J. Mech. Cont.& Math. Sci., Special Issue, No.-5, January (2020) pp 40-5.*

# A Patent on Power factor correction with Buck converter for BLDC motor Drive. Application number: 202241073208 A . Date:30/12/2022.

#:An Accurate Frequency Tracking Method Based On Short Current Detection. In IJRIT, Volume 2 Issue 4. Sep-2015

#: A Smart Strategy for Voltage Control Ancillary Service. In IJRIT, Volume2 Issue 4. Sep-2015

#: Design of Islanding Detection Using Phase latched Loops in Three phase Grid

Interfacing Powers. In IJRIT, Volume 2 Issue 4. Sep-2015

### Personal Details:

Father's Name : M.Venkat Reddy  
Date of Birth : 12 - 03 -1987  
Sex : Male.  
Nationality : Indian.  
Marital Status : Married.  
Languages Known : Speak : Telugu, and English.  
Read : Telugu, English.  
Write : Telugu, English.

Permanent Address : Address-1: H.No: 2-48  
Pragatoor (p),  
Alampur (m),  
Mahaboob Nagar (Dist)  
Andhra Pradesh-509153

Present Address : Address-2:  
H.No: 96/3-8-9-288-A-10,  
Pulipati Nagar, Santhosh Nagar ,

**Declaration:**

I hereby declare that the above information furnished is genuine and true to the best of my knowledge.

**(M.Madhusudhan Reddy)**